Errata: CS8900A Revision B

CS8900A Product Datasheet (DS271PP3, MAR '99)

1. On page 111 change the power supply current while active for 5V to 110mA maximum, for 3.3V to 100mA maximum.

2. On page 15, ELCS pin, change "If external LA decode logic is not needed ELCS should be tied low" to "If external LA decode logic is not needed ELCS should be left floating." Leaving this pin floating will cause bit B, Elpresent, Register 16 to be set, even though no external logic is present.

    The CS8900A Rev B will attempt to drive the ELCS pin high if it is tied low. This results in 50mA of additional supply current. This will be fixed in Rev C of the CS8900A.

If there are any questions concerning this information,
Please contact: James Ayres at 512.912.3827